Getting Started with the SoCKit Board

Users can reference the document SoCKit_GSG.pdf found in the SoCKit System CD manual folder. This guide contains a quick overview of the hardware and software setup including step-by-step procedures from installing the necessary software tools to using the SoCKit board.

The main topics that this guide covers are listed below:
1. Software Installation: Installing Quartus II and EDS.
3. Perform FPGA System Test: Downloading an FPGA SRAM Objective File (.sof).
4. Running Linux on SoCKit Board.

Starting Your First FPGA Design

Users can reference the document My first FPGA.pdf found in the SoCKit System CD manual folder. This document describes the complete FPGA design flow, including:
1. Creating a new Quartus II project.
2. Adding user logic and utilizing mega-core IPs.
3. Downloading an .sof file to the FPGA to view the result.

Getting Help

For further discussion, support, and resources, please go to:
- [http://sockit_support.terasic.com](http://sockit_support.terasic.com)
- [http://rocketboards.org](http://rocketboards.org)

What's in the Box?

1. SoCKit Board
2. SoCKit Quick Start Guide
3. Power DC Adapter (12V)
4. Ethernet Cat 5e Cable
5. Type A to Micro B USB Cable x2

www.terasic.com
## Default Jumper and Switch Settings

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<th>Item</th>
<th>Component</th>
<th>Function</th>
<th>Setting</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>1</td>
<td>SW4</td>
<td>JTAG Chain Select</td>
<td>Set HPS_EN to OFF, Set HSMC_EN to ON</td>
<td>Enable HPS, Bypass HSMC</td>
</tr>
<tr>
<td>2</td>
<td>JP2</td>
<td>Adjust the I/O Standard of the FPGA/HSMC pins</td>
<td>Short Pin 5 and 6</td>
<td>The default is 2.5V</td>
</tr>
<tr>
<td>3</td>
<td>J17, J19, J18</td>
<td>Select the boot source for the HPS</td>
<td>BOOTSEL0 Logic 1: Short Pin 1 and 2, BOOTSEL1 Logic 0: Short Pin 2 and 3, BOOTSEL2 Logic 1: Short Pin 1 and 2</td>
<td>3.0 V SD/MMC Flash memory</td>
</tr>
<tr>
<td>4</td>
<td>J15, J16</td>
<td>Select the clock settings for the HPS</td>
<td>CLKSEL0 Logic 0: Short Pin 2 and 3, CLKSEL1: Short Pin 2 and 3, CLKSEL1 Logic 0: Short Pin 2 and 3</td>
<td>Settings are bootselection dependent</td>
</tr>
<tr>
<td>5</td>
<td>SW6</td>
<td>FPGA Configuration Mode Switch</td>
<td>Set SW6.1~SW6.5 to &quot;01001&quot;</td>
<td>ASx4 mode</td>
</tr>
</tbody>
</table>

## Perform Power-on Test

1. Connect the power adapter to the power jack of the SoCKit.
2. Press the power on button.
3. Make sure SW6 is set to ASx4 mode (SW6.1~SW6.5 "01001")
4. All the FPGA User LEDs are flashing.
5. Connect a VGA monitor to D-sub connector(J10) and the VGA monitor will display the board image.

### Contents of the SoCKit System CD

Users can download the SoCKit System CD from this link:

[http://socket_support.terasic.com](http://socket_support.terasic.com)

### SoCKit System CD Contents

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